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This is to certify that **Prashant Shirke** has published a research paper entitled 'VLSI architecture of configurable low complexity hard decision Viterbi decoder' in the International Journal of Scientific Research in Science and Technology (IJSRST), Volume 3, Issue 4, May-June-2017.

This Paper can be downloaded from the following IJSRST website link https://ijsrst.com/IJSRST1733212 IJSRST Team wishes all the best for bright future

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