

International Journal of Scientific Research in Science and Technology

Print ISSN: 2395-6011 | Online ISSN: 2395-602X

[UGC Journal No : 64011]

Peer Reviewed and Refereed International Scientific Research Journal

Scientific Journal Impact Factor: 8.62

Certificate of Publication

Ref: IJSRST/Certificate/Volume 3/Issue 6/1394 29-Aug-2017

This is to certify that V. Kiran Kumar Reddy, K. Sravan Kumar have published a research paper entitled 'High Performance and Area Efficient DSP Architecture using Dadda Multiplier' in the International Journal of Scientific Research in Science and Technology (IJSRST), Volume 3, Issue 6, July-August-2017.

This Paper can be downloaded from the following IJSRST website link

https://ijsrst.com/IJSRST1736109

IJSRST Team wishes all the best for bright future

Editor in Chief

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