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This is to certify that **Sankisha S. Moon, S. M. Sakhare** have published a research paper entitled '*Review on Design Approach for FPGA Implementation of 16-Bit Vedic Multiplier*' in the International Journal of Scientific Research in Science and Technology (IJSRST), Volume 3, Issue 6, July-August-2017.

This Paper can be downloaded from the following IJSRST website link

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