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08-Sep-2017

This is to certify that R. Jai Ganesh, A. Bhuvanesh have published a research paper entitled 'Design and simulation of closed loop controller for SEPIC converter to improve power factor' in the International Journal of Scientific Research in Science and Technology (IJSRST), Volume 3, Issue 7, September-October-2017.

This Paper can be downloaded from the following IJSRST website link

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IJSRST Team wishes all the best for bright future

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