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04-Dec-2017

This is to certify that **P. Kanimozhi, S. Ramalingam, S. Murugesan, N. Ramya** have published a research paper entitled '**Input Vector Observing Simultaneous BIST Architecture Utilizing SRAM Cells**' in the International Journal of Scientific Research in Science and Technology (IJSRST), Volume 3, Issue 8, November-December-2017.

This Paper can be downloaded from the following IJSRST website link

<https://ijsrst.com/IJSRST1738123>

IJSRST Team wishes all the best for bright future

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