

Investigation of Circuit Breaker Switching Transients for Shunt Capacitor Using Matlab/Simulink

Seema S. Kalwaghe^{*1}, Prof. Mukund R. Salodkar²

^{*1}M.E Electrical Engineering (EPS) Department, G. H Raisoni College of Engineering, Amravati, SGBAU University, Amravati, Maharashtra, India

²M.E Electrical Engineering (EPS) Department, G. H Raisoni College of Engineering, Amravati, SGBAU University, Amravati, Maharashtra, India

ABSTRACT

It is common practice to install shunt capacitors to improve the power factor and voltage profile at all voltage levels in the power system. Almost all type of load including domestic user's appliances are inductive. Due to low water level agricultural load are also increasing. All these conditions lead to poor power factor and large amount of reactive power flows through the transmission line and voltage drop occurs. Hence, in several areas low voltage pockets are created. To improve this low power factor, adequate compensation is to be provided at load end point. Due to increasing load/consumers it is very difficult to insist every consumer to provide compensating device. Ultimately power transmission companies have decided to provide the capacitor bank at the feeding substation at various voltage levels such as 11KV, 33KV and EHV levels also. And now a day it is well accepted practice to install a capacitor bank at the feeding substation for improving the power factor of the system. The major problem with the capacitor bank is the interrupter failure i.e. the failure of circuit breaker employed for its switching. It is reported by power distribution and transmission companies that breaker failure occurs before its specified number of operation. In order to understand this problem, I thought of taking a topic of dissertation work, which relates to the failure of circuit breaker employed for capacitor switching.

Keywords: Inrush Current, Capacitor Bank, Shunt Capacitor Switching.

I. INTRODUCTION

It is common practice to install shunt capacitors to improve the power factor and voltage profile at all voltage levels in the power system. Due to low water level agricultural load are increasing. All these conditions lead to poor power factor and large amount of reactive power flows through the transmission line and voltage drop occurs. The major problem with the capacitor bank is the interrupter failure i.e. the failure of circuit breaker employed for its switching. It is reported by power distribution and transmission companies that breaker failure occurs before its specified number of operation. Various reasons for failure of breaker can be broadly categorized as below.

IV.

1. Less effective medium in interrupting chamber
2. Deterioration of circuit breaker contacts due to frequent switching operation of capacitor bank
3. Effect of capacitor bank charging in-rush current which can be of very high magnitude.

4. Out-rush current due to fault on supply bus or very to bus.
5. Effect of restricting voltage (over voltage).
6. Effect of L-C Resonance

All these effects are to be studied and simulate these conditions by developing soft computing model.

III. AIM

Simulation of capacitor bank switching actions of a shunt capacitor bank connected to 3 phase power supply through extra high voltage circuit breaker with all standard specifications of normal circuit breaker and to establish that normal specification circuit breaker is not adequate for capacitive load switching.

IV. OBJECTIVES

1. To study and differentiate between inductive load switching and capacitor bank switching.

- To study the behaviour of circuit breaker during capacitor bank switching and switching during fault by developing soft computing model.
- To establish that normal specification circuit breaker is not adequate for capacitive load switching.

V. METHODOLOGY

The methodology proposed for this dissertation work is given as below.

It is proposed to consider 132KV level substation as a model substation, having 132 KV bus and accordingly it is proposed to design in soft model. Bus is connected to 2 Nos. of 132 /11KV 25 MVA transformers and 2 Nos. of 132 KV lines emanating from bus having 150 Amps load on each line with PF 0.80. Each transformer is drawing 43-44 amps HV side current with 0.80PF. For improvement in power factor, 20 MVAR shunt capacitor bank is connected to bus. All the circuit breakers connected for lines, transformers and capacitor bank have same name plate details as below.

BREAKER SPECIFICATION

- Normal voltage-145KV(Max)
- Normal current-1600Amps.
- Lightning Impulse withstand voltage-650KV.
- Short circuit Breaking current-31.5Amps/sec.
- Line charging Breaking current-50Amps.
- Operating sequence-0.035-CO-3Min-CO.
- First pole to clear factor-1.3.
- Power Frequency withstand voltage-275KV
- DC component-51%.
- Making capacity-80KA.

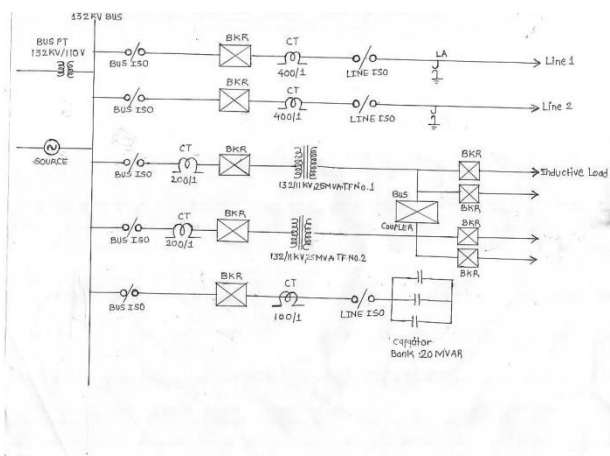
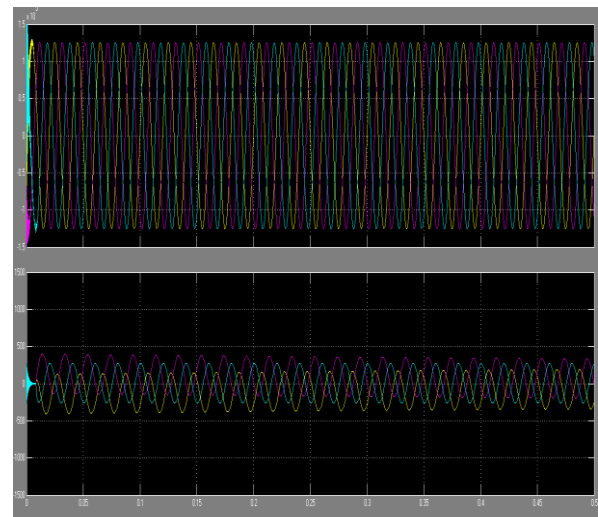
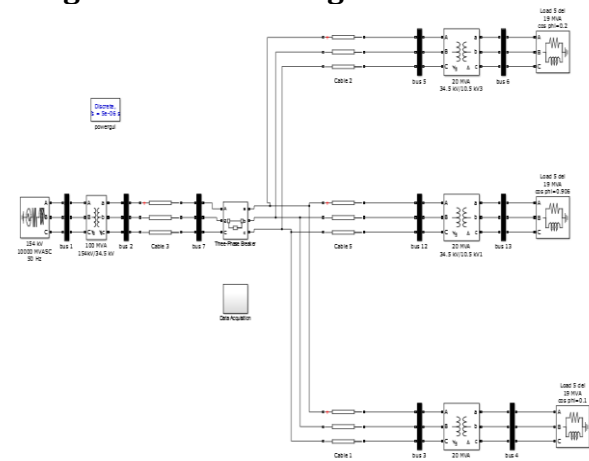


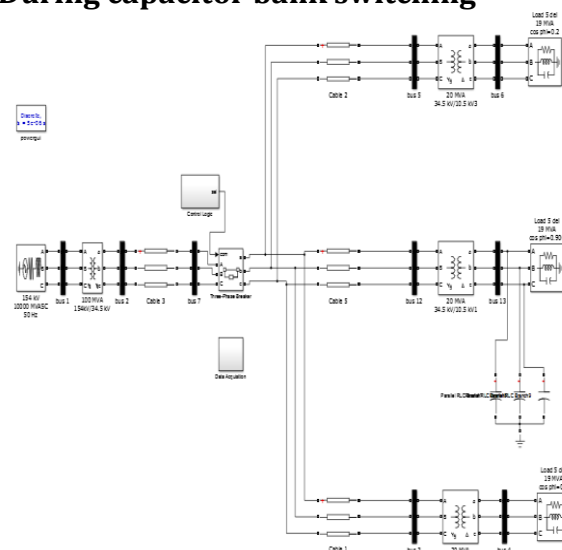
Figure 1 : Single Line Diagram of Proposed 132/11 Kv Model Substation

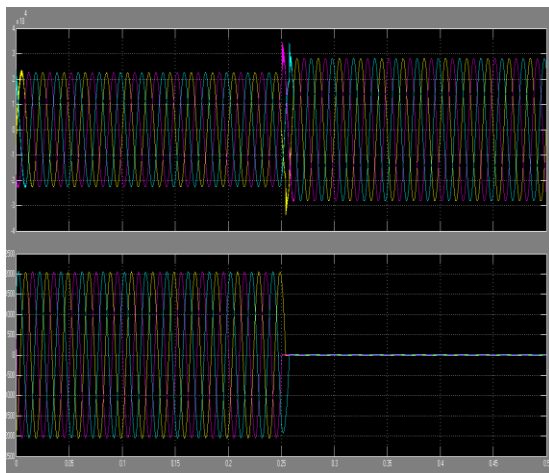
VI. RESULTS AND DISCUSSION

During normal switching



During capacitor bank switching





Switching during fault

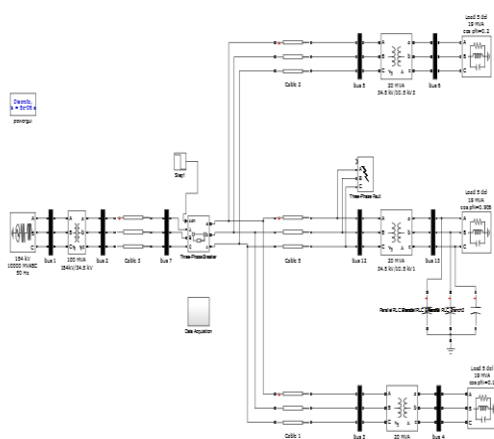


TABLE I
TABLE SHOWING RESULTS OF RESPECTIVE
OPERATING CONDITIONS

S r. N o.	Oper ating Con d ition	Nor mal volt age	Load	Magni tude (% DC compo nent)	Powe r Fact or	Capacity of capacito r bank to installed (Mvar)	Rem ark
--------------------	------------------------------------	---------------------------	------	---	-------------------------	---	------------

1	Normal Con d ition	154 KV/ 54K V	19M VA	-	0.2	-	-
2	Capa citor Bank Char ge	154 KV/ 54K V	19M VA	71%	0.9	20	Brea ker failed
3	Fault Con d ition	154 KV/ 54K V	19M VA	87%	0.9	20	Brea ker failed

VII. CONCLUSION

Providing capacitor bank to power feeding sub-station is beneficial and also increases transformer MVA efficiency. But, majority of capacitor banks are non operative due to problems in switching circuit breaker. if adequate switching device is provided which can sustain all the stresses of capacitive switching then all the capacitor banks can be made operative and stresses on power system can be reduced to much extent.

VIII. REFERENCES

- [1]. Gustavo Brunello, Dr. Bogdan Kasztenny, Craig Wester, Shunt Capacitor bank Fundamentals and Protections,” 2003 conference for Protective Relay Engineers-Texas A&M University April 8-10,2003, IEEE Guide for the Protection capacitors Banks.
- [2]. Florin korner, Manfred Lindmayer, Michael Kurrat (Institute for Hochspannungstechnik and Elektrische Energieanlagen, Technische Universitat Braunschweig Germany), Dietmar Gentsch, ABBA Gcalor Emag Mittels Pannungsprodukte, oberhausenstrabe 33,40472 Ratingen, Germany. - Synthetic test of Capacitive current switching using a test vessel”,1_4244_0192_5/06/\$20.00 2006IEEE.
- [3]. Lutz GEBHARDT, Bernhard RICHIER ABB_Switzerland -surge arrester application of MV-capacitor banks to mitigate problems of switching restrikes”,19thInternational conference on electricity distribution Vienna,21-24 May - 2007.

- [4]. Ying-Yi Hong and - Yaunchen. -Locating switch capacitor using a wavelet transform and hybrid principle component analysis network”, 0885-8977/\$25.00 2007 IEEE.IEEE transactions on power delivery.vol.22. No-2, April_2007.
- [5]. Joe Rostran, P.E. 3/12/2007 Southern states LLC, Hampton, Ga. Editor: David Childress 8/12/07, southern states LLC Hampton, GA -Analysis of example capacitor bank switching solution and recommendations for revision”.
- [6]. Mohd Shamir Ramli, M. Eng. thesis, QUT, 2008 on -Investigation of circuit breaker switching transients for shunt reactors and shunt capacitors”, Queensland university of technology.
- [7]. S.J. Kulas, Faculty of Electrical Engineering, Warsaw university of technology GG130, PL. Politechniki_1,00_662 Warsaw, Poland. - Capacitor switching techniques”, International conference on renewable energies and power quality (ICREPQ '09), Valencia(Spain),15th - 17th April,2009.
- [8]. Shehab Abdul wadood Ali, Department of Physics college of saber, university of Aden, Adenyemen - Capacitor banks switching transients in power system”, CSC Canada Energy Science and Technology, vol-2, NO.2,2011.
- [9]. Gopal Gajjar, A. M. Kulkarni and S. A. Soman. Department of Electrical Engineering, Indian institute of technology Powai, Bombay, Mumbai(India), Paper submitted to the International conference on Power Systems (IPST 2013) in Vancouver, Canada July 18-20,2013. - Interaction of Capacitor Bank Inrush Current Limiting Reactor and Medium Voltage Vacuum Circuit Breakers”.
- [10]. Salil S. Sabade, Himanshu J. Bahirat, Dr. Bruce A. Mork, Department of Electrical and Computer Engineering, Michigan Technological University, Houghton, MI 49931.Mythili Chaganti, Xcel Energy Inc.414 Nicollet Mall, Minneapolis, MN55401 on -Shunt Reactor Switching Transients at High Compensation Levels”.
- [11]. Shunt Capacitor Bank Switching Transients A Tutorial and case study”, carried out by Govind Gopalkumar, Huihuayan, Dr. Bruchs A. Mork of Michigan Technological University Houghtan, MI 49931 and Kalyan K. Mustabhi, Northern states Power Company, Minneapolis, MN-55401.
- [12]. Power Systems Capacitors, Ramswamy Natarajan.