

Predictive Torque Control Scheme for Three-Phase Four-Switch Inverter Fed Induction Motor Drives with DC-Link Voltages Offset Suppression by Using Fuzzy Logic Controller

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ABSTRACT

This paper proposes a three-phase four-switch (B4) inverter, having a fewer amount of switches, be primarily introduced for the possibility of reducing the inverter cost, and it proved to be extremely useful as it can be used in fault tolerant control to avoid the open/short circuit fault of the six-switch three-phase (B6) inverter. However, the balance among the phase currents collapses because of the change of the two dc-link capacitor voltages; so its application is limited. This document suggests a predictive torque control (PTC) scheme for the B4 inverter-fed induction motor (IM) with the dc-link voltage offset suppression by using fuzzy logic controller. The voltage vectors of the B4 inverter under the variation of the two dc-link capacitor voltages are determined for exact estimate and control of the torque and stator flux. The three-phase currents are forced to stay balance by directly controlling the stator flux. The voltage offset of the two dc-link capacitors is demonstrated and prohibited in the predictive point of view. A fuzzy controller is used for better control of speed and to minimize the THD in stator currents. A significant measure of simulation and experimental results are displayed to approve the proposed control scheme.

Keywords : Cost function, current unbalance, four-switch inverter, induction motor (IM) drives, model predictive control (MPC).

I. INTRODUCTION

Throughout the years, the traditional three-phase voltage source inverter with six switches (B6) has been found across the board mechanical applications in different structures, for example, motor drives and active filters. However, in specific applications, a further cost diminishment for inverter arrangement is considered by clients. To accomplish this objective, the three-phase inverter with as it were four switches was proposed by Van der Broeck and Van Wyk [1] with the end goal of minimizing the segments' cost, and it is named four-switch three-phase (B4) inverter in examination with the B6 one, as appeared in Fig. 1. In spite of the fact that this kind of cost lessening is to the detriment of output execution, the B4 inverter can be used in fault tolerant control to solve the open/short-circuit of the B6 inverter. The possibility of the B4 inverter connected to fault tolerant control is extremely profitable in a few basic events, for example, rail traction, and it has therefore pulled in light of a legitimate concern for some specialists [2]–[4].

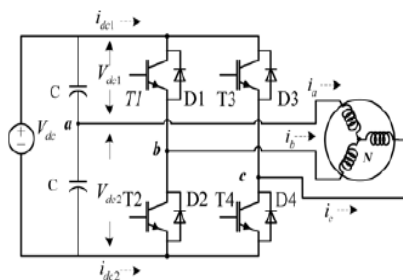


Figure 1. Circuit diagrams of a B4 inverter-fed induction motor drive.

The four-switch inverters are known to have a few disservices contrasted with typical six-switch inverters: the voltage use component is split contrasted with the six-switch inverter.

In the PTC, the entire model and future conduct of the inverter-fed drives are considered. A cost work relating to torque and flux errors lessening is characterized to assess the impacts of every voltage vector and the one minimizing the cost capacity is chosen. Notwithstanding the remarkable execution of B6 inverter-nourished drives in view of the PTC, PTC for B4 inverter-fed

drives did not get numerous considerations to the specialists. Some reproduction aftereffects of PTC for the B4 inverter-fed drives copying the B6 case were completed. Be that as it may, the dc-link voltages fluctuation, which is the intrinsic feature of the B4 inverter, was not considered[13]. On account of the split switch states relating to B6 one, the constant usage time taken a toll for the PTC plot in B4 inverter is decreased in an inspecting period. This paper is sorted out as takes after. In Section II, the model of B4 inverter and induction motor (IM) is illustrated.

PARAMETERS OF THE INDUCTION MACHINE:

φ_r	→	Rotor flux.
φ_s	→	Stator flux.
τ_r	→	Rotor Torque Constant.
L_m	→	Magnetizing Inductance.
L_s	→	Stator Leakage Inductance.
L_r	→	Rotor Leakage Inductance.
k_r	→	Rotor coupling element.
σ	→	Total Leakage Factor.
λ_{dc}	→	Weight factor of the dc-link capacitor.
λ_0	→	Weight factor.
T_e	→	Electromagnetic Torque.
T_{enom}	→	Rated Torque.
i_s	→	Stator current.
i_r	→	Rotor current.

TABLE I. SWITCHING FUNCTION AND THE OUTPUT VOLTAGES OF THE B4 INVERTER

Stat es		Swit ch On		output Voltages		
				VaN VcN	VbN	
S_b	S_d					
0	0	T_2	T_4	$\frac{2Vdc_2}{3}$	$-\frac{Vdc_2}{3}$	$-\frac{Vdc_2}{3}$
0	1	T_2	T_3	$\frac{V_{dc2}-V_d}{3}$	$-\frac{2V_{dc2}+V_d}{3}$	$\frac{2V_{dc1}+V_{dc2}}{3}$
1	0	T_1	T_4	$\frac{V_{dc2}-V_d}{3}$	$\frac{2V_{dc1}+V_{dc2}}{3}$	$-\frac{2V_{dc2}+V_d}{3}$
1	1	T_1	T_3	$-\frac{2Vdc_1}{3}$	$\frac{Vdc_1}{3}$	$\frac{Vdc_1}{3}$

The proposed PTC control plot with capacitor voltage suppression is clarified in Section III. The weight figures

the fetched capacity are dissected and talked about in Section IV. In Section V, exploratory outcomes are appeared. The conclusions are introduced in Section VI.

II. DESIGNING OF THE B4 INVERTER AND IM

A. Voltage Vector of a B4 Inverter

The B4 topology comprises of a two-leg inverter, as outlined in Fig. 1. The dc-link is part into two voltage sources, to the center of which one load phase is associated. For advantageous examination, the inverter is considered for execution by perfect switches (T1–T4) (i.e., with no dead time and no immersion voltage drop).

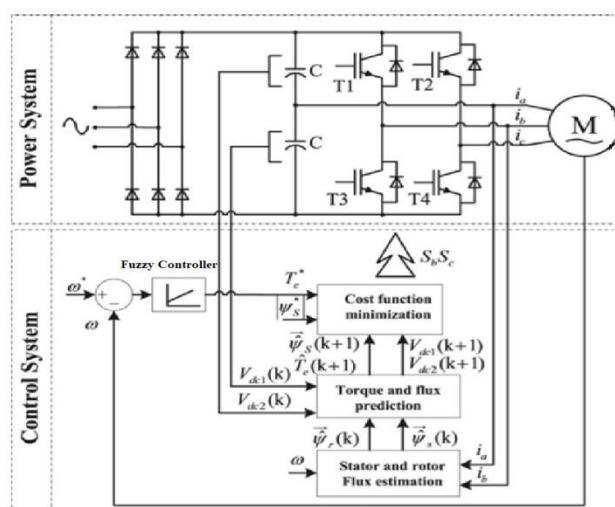


Figure 2. Structure of the B4 inverter-fed IM drive based on the PTC scheme.

III. PROPOSED SCHEME FOR THE B4 INVERTER-FED IM DRIVE

In the proposed scheme, the internal loop is a stator flux and electromagnetic torque controller in based on PTC, while the external speed loop is a Fuzzy logic controller. The structure of the B4 inverter-sustained IM drive-based PTC scheme is appeared in Fig.2.

A. Flux Estimation

It is advantageous to note that the voltage-demonstrate based flux estimator utilizing the order voltages can around gauge the stator flux in the B6 case.[18]

Here, a present model-based flux estimator utilizing moment streams what's more, speed signs are embraced

in the proposed plot and the estimations of the stator flux $\vec{\psi}_s$ and the rotor flux $\vec{\psi}_r$ at the present testing step k are required.

Utilizing Euler-based discretization as discrete conditions of the rotor and stator flux estimation are as per the following:

$$\varphi_r(k) = \frac{\tau_r}{T_s(1-j\omega\tau_r)} \varphi_r(k-1) + \frac{L_m}{1-j\omega\tau_r} i_s(k) \quad (1)$$

$$\varphi_s(k) = K_r \varphi_r(k) + \sigma L_s i_s(k) \quad (2)$$

Where T_s relates to the testing time, $k_r = L_m/L_r$ is the rotor coupling element and $\sigma = 1 - (L_m/L_s L_r)$ is the aggregate spillage consider. As it is clear to find in (1), the rotor flux estimation is gotten without utilizing the order voltages.

B. Electromagnetic Torque and Stator Flux Prediction

Since the control factors are stator flux is estimated above in equation (2) and electromagnetic torque is derived below as,

$$T_e(k+1) = \frac{3}{2} p \operatorname{Im}\{\varphi_s(k+1) i_s(k+1)\} \quad (3)$$

The forecast articulation of the stator current is $(k+1)$ is acquired utilizing the proportional condition of the stator progression of an induction machine.

The forecast condition of the stator current is at the moment $k+1$ is acquired.

$$i_s(k+1) = \left(1 + \frac{T_s}{\tau_\sigma}\right) i_s(k) + \frac{T_s}{\tau_\sigma + T_s} \left\{ \frac{1}{R_\sigma} \left(\left(\frac{k_r}{\tau_r} - jk_r\omega \right) \varphi_r(k) + v_s(k) \right) \right\} \quad (4)$$

Once the forecasts of the stator flux (2) and the stator current (4) are acquired, the forecast of the electromagnetic torque can be ascertained in (3).

C. Cost Function Optimization

The following stride in predictive control is the optimization of an fitting control law that is characterized as a cost work. The structure type of the cost function is given as takes after:

$$g_i = \frac{|T_e^* - T_e(k+1)|}{T_{enom}} + \lambda_0 \frac{\|\varphi_s^* - \varphi_s(k+1)\|}{\|\varphi_s\|_{nom}} \quad (5)$$

$$i \in \{1, 2, 3, 4\}.$$

Where i indicates the index of the stator voltage vector utilized to figure the expectations $T_e(k+1)$ and $\vec{\psi}_s(k+1)$, respectively. The component λ_0 signifies a weight figure. At long last, the improvement step is completed, and the inverter voltage vector that minimizes the cost capacity is chosen as the ideal exchanging state for the following testing time frame $k+1$, hence the ideal torque and flux control is accomplished.

D. Time-Delay Compensation

It is outstanding that there is one-phase delay in advanced usage. In other word, the voltage vector chose at the moment time k won't be connected until the moment time $k+1$. To wipe out this deferral, the value at the moment time $k+2$ ought to be utilized as a part of (18) as opposed to the moment time $k+1$.

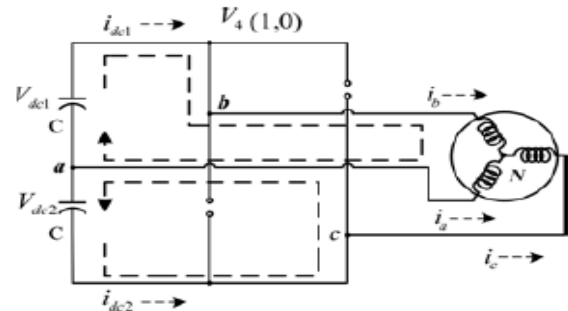
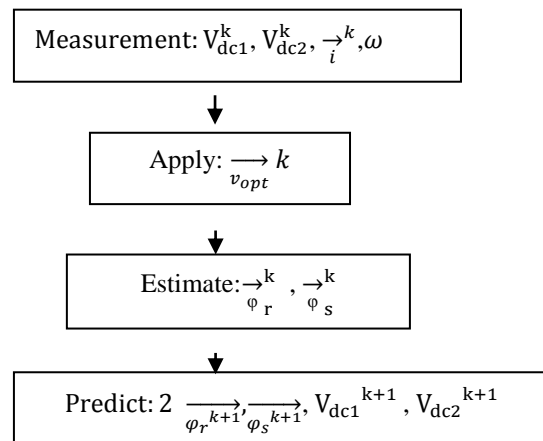


Figure 1. Current paths in the switching state V4.

E. DC-Link Voltage Offset Suppression

The improper starting phase angle of phase "a" current or the irregularity current flowing in the two capacitors will bring about voltage deviation.



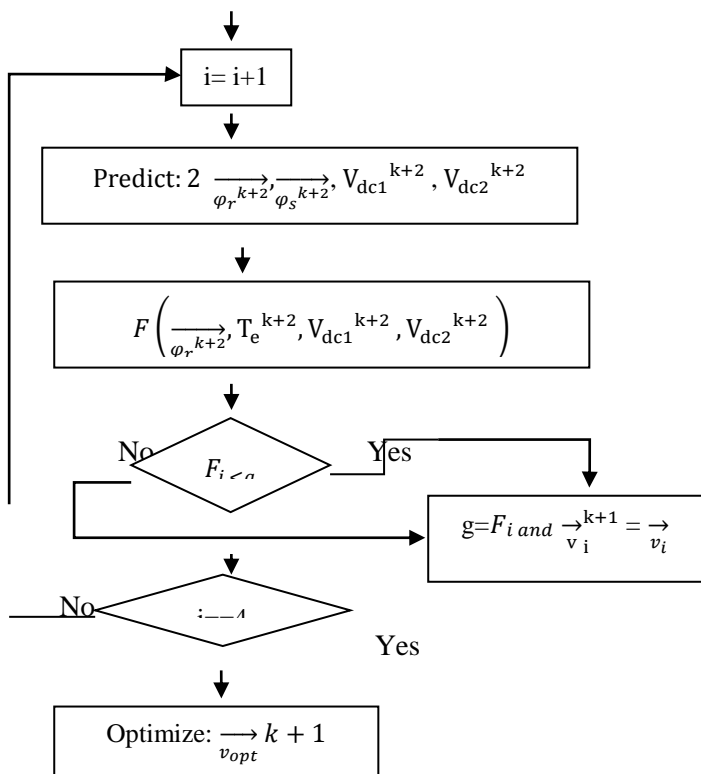


Figure 5. Implementation Flowchart Of The Proposed Scheme.

The cost work, including the voltage balance concealment, is given by adding a third term to the cost work (19)

$$\begin{aligned}
 g_i &= \frac{|T_e^* - T_e(k+2)|}{T_{e_{nom}}} \\
 &+ \lambda_0 \frac{||\varphi_s^*| - |\varphi_s(k+2)||}{||\varphi_s||_{nom}} \\
 &+ \lambda_{dc} \frac{|V_{dc1}(k+2) - V_{dc2}(k+2)|}{V_{dc}}, \\
 &i \in \{1, 2, 3, 4\}
 \end{aligned}$$

Where V_{dc} is dc-link voltage, which can be gotten by $V_{dc} = V_{dc1} + V_{dc2}$, λ_{dc} is the weight variable of the dc-link capacitor voltage offset suppression. The minimization is finished by a comprehensive inquiry for all achievable voltage vectors. The proposed control plot can be executed in the accompanying grouping (see Fig. 5). The superscript k , $k + 1$, and $k + 2$ mean the factors' value at testing time k , $k + 1$, and $k + 2$, separately.

IV. FUZZY LOGIC

As of recent, the number and assortment of uses of fuzzy logic have expanded essentially. The applications go from consumer products, for example, cameras, camcorders, clothes washers, and microwave stoves to

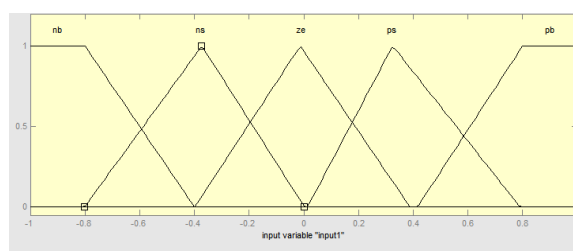
mechanical process control, restorative instrumentation, choice emotionally supportive networks, and portfolio choice. To comprehend why utilization of fuzzy logic has developed, you should first understand what is implied by fuzzy logic

Tables : Rule base for Five Membership Function

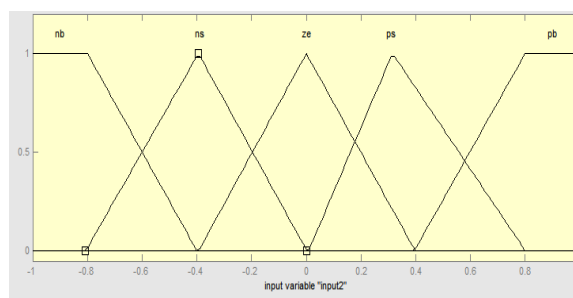
Change in Error ($\frac{dV}{dt}$)	Error (Voltage)				
	NB	NS	ZE	PS	PB
NB	PB	PB	PB	PB	PB
NS	PB	PB	PS	PS	PB
ZE	ZE	ZE	ZE	PS	PB
PS	NS	NS	NS	NS	PB
PB	PB	PB	NB	NB	PB

FUZZY MEMBERSHIP FUNCTION:

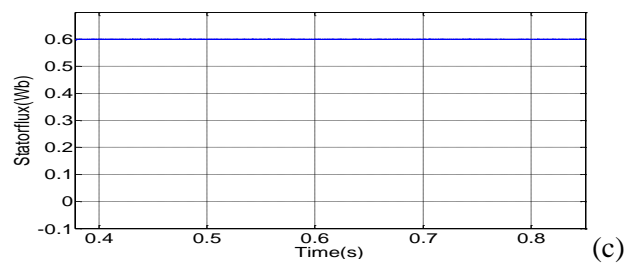
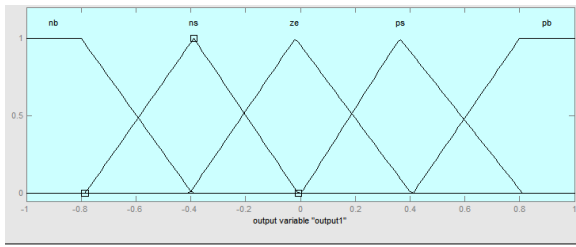
Membership functions for input1:



Membership functions for input2:

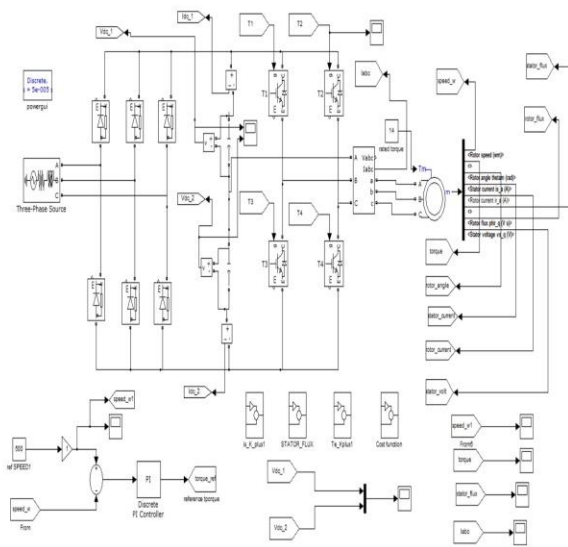


Membership function for output :

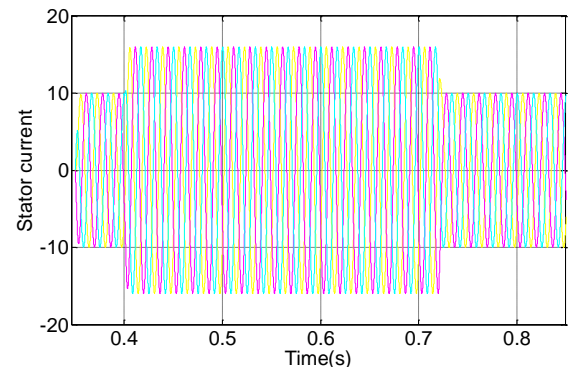


V. SIMULATION CIRCUIT:

PI CONTROLLER:



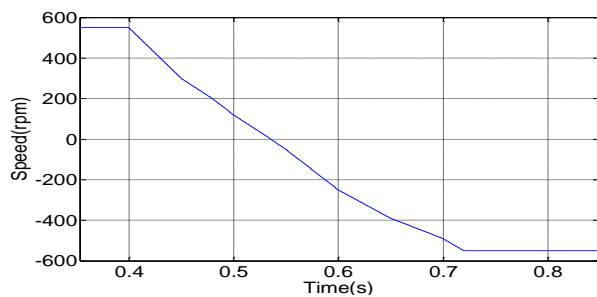
Stator flux



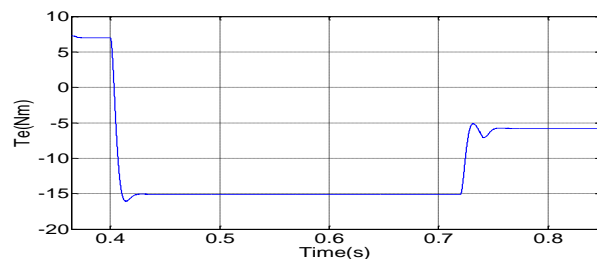
(d) Stator current behavior

Fig.6. Simulated waveforms using the conventional scheme in a B4 inverter fed IM. (a) Speed, (b) developed torque, (c) stator flux, and (d) statorcurrent behaviors during a speed-reversal maneuver at 50% rated load torque ($\lambda_0 = 3$).

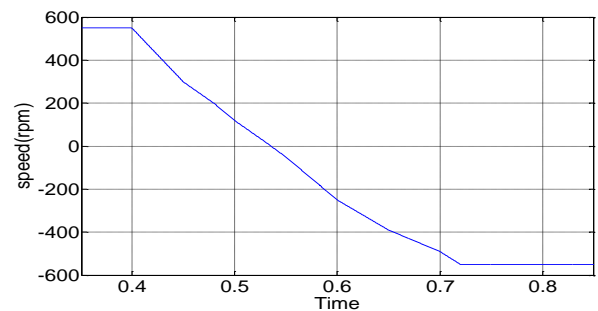
VI. CONVENTIONAL RESULTS



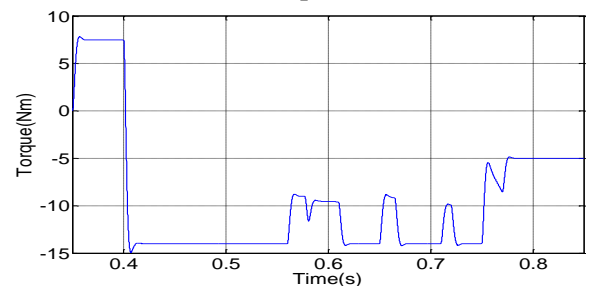
(a) Speed



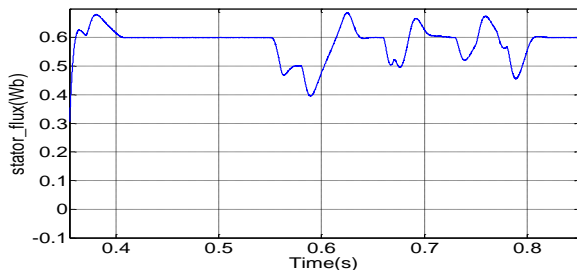
(b) Developed torque



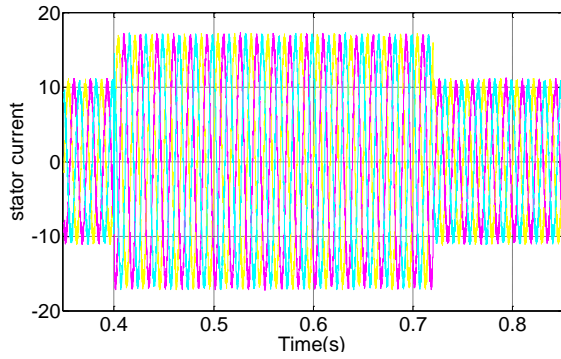
(a) Speed



(b) Developed torque

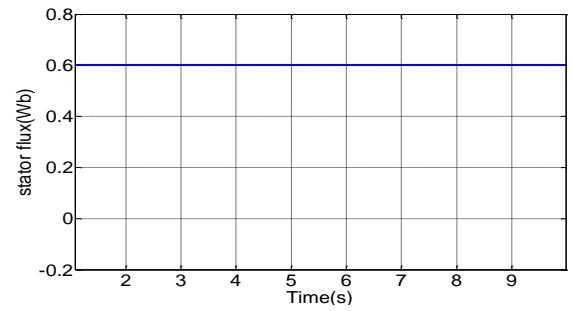


(c) Stator flux

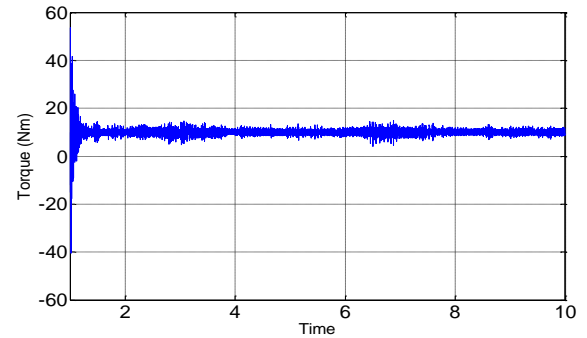


(d) Stator current behavior

Fig.7. Simulated waveforms using the conventional scheme in a B4 inverter fed IM. (a) Speed, (b) developed torque, (c) stator flux, and (d) stator current behaviors during a speed-reversal maneuver at 50% rated load torque ($\lambda_0 = 1$).

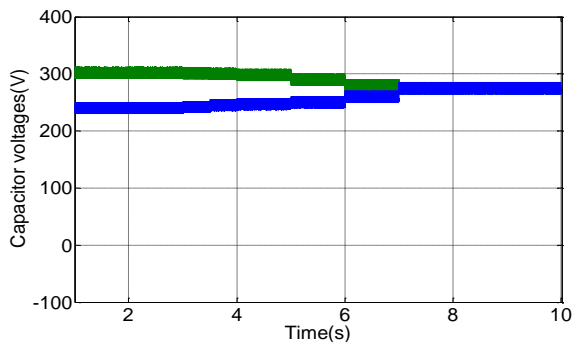


(c) Stator flux

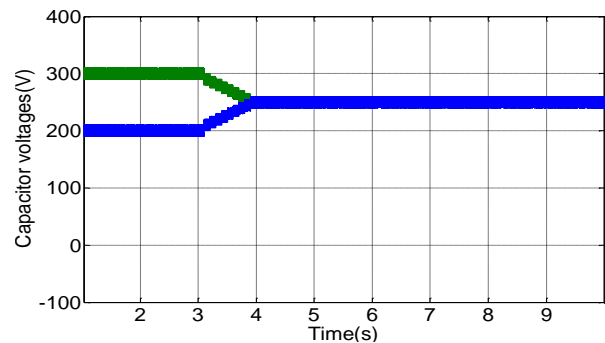


(d) Developed torque behavior

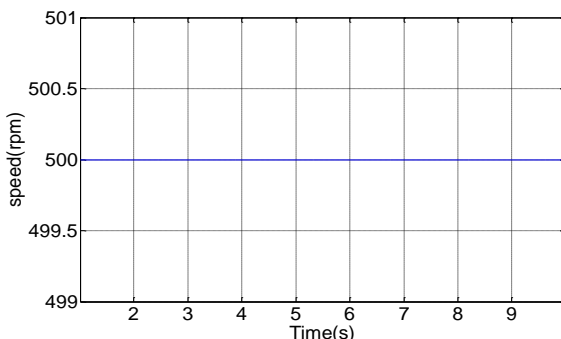
Fig.8. Simulated two capacitor voltages with offset suppression method.(a) Capacitor voltages, (b) speed, (c) stator flux, and (d) developed torque behaviors during a voltage suppression method applied ($\lambda_{dc} = 1000$)



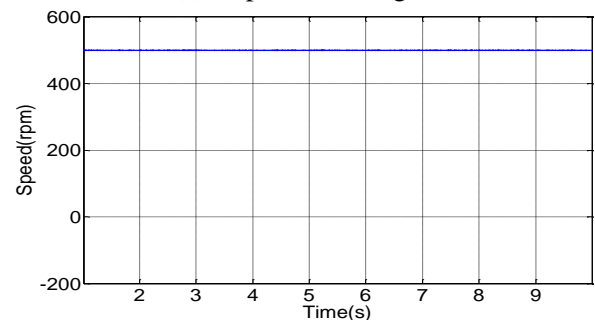
(a) Capacitor voltages



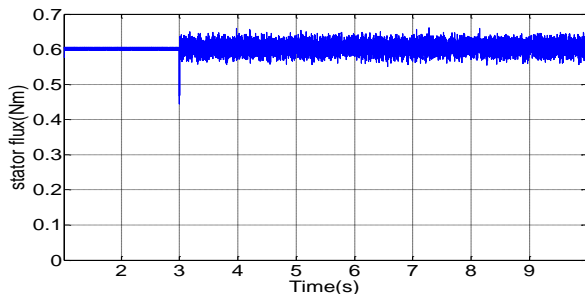
(a) Capacitor voltages



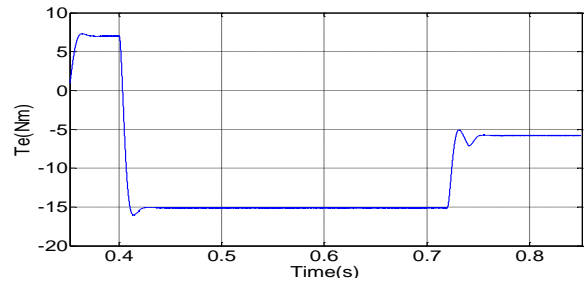
(b) Speed



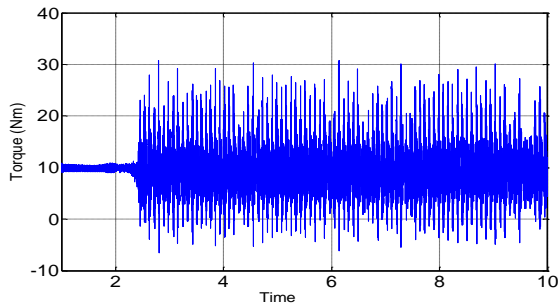
(b) speed



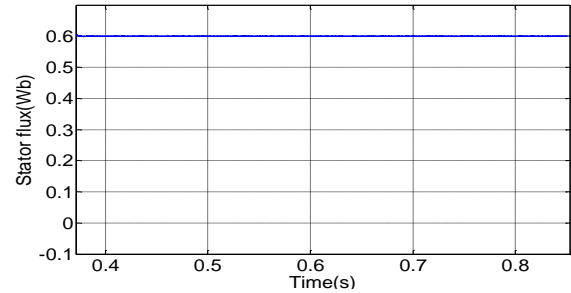
(c) stator flux



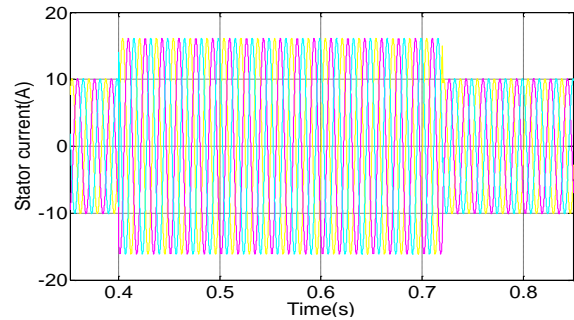
(b) Developed torque



(d) developed torque behaviour



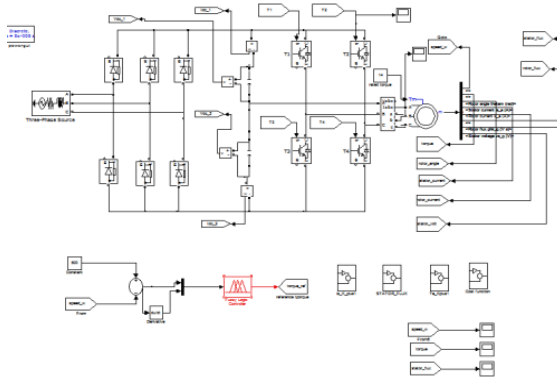
(c) Stator flux



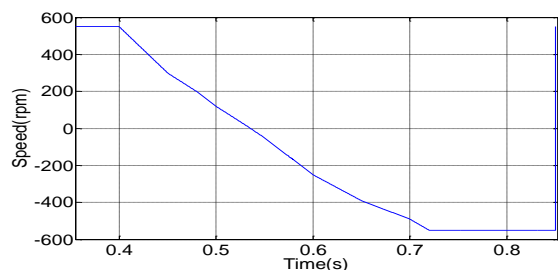
(d) Stator current

Fig.9. Simulated two capacitor voltages with offset suppression method.(a) Capacitor voltages, (b) speed, (c) stator flux, and (d) developed torque behaviors during a voltage suppression method applied ($\lambda_{dc} = 2000$).

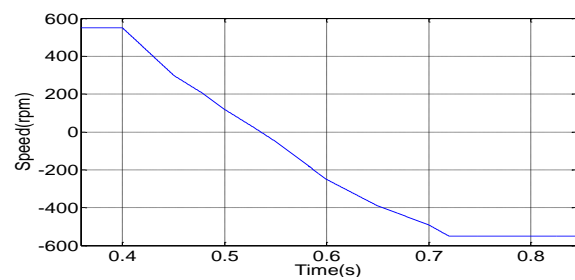
FUZZY LOGIC CONTROLLER:



PROPOSED RESULTS:

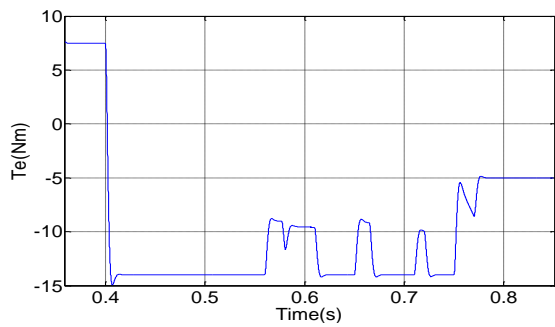


(a) Speed

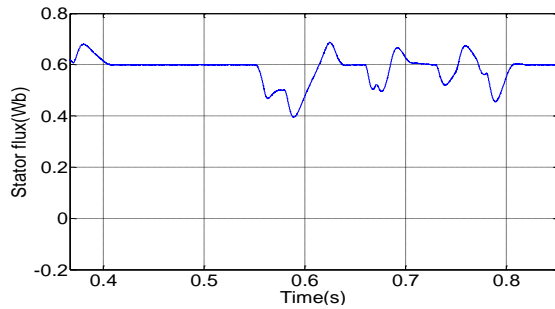


(a) Speed

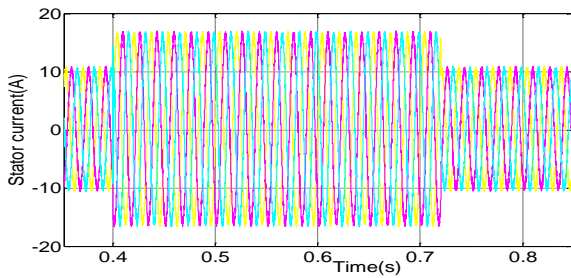
Fig.9 Simulated waveforms using the proposed scheme in a B4 inverter fed IM behaviors during a speed-reversal maneuver at 50% rated load torque ($\lambda_0 = 3$).



(b) Developed torque

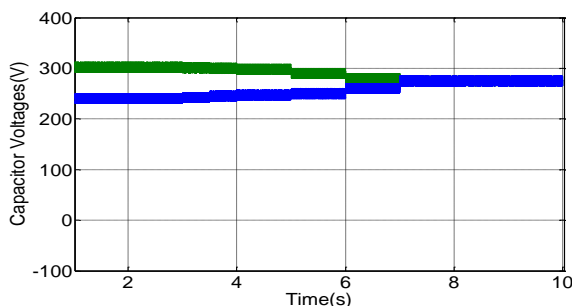


(c) Stator flux

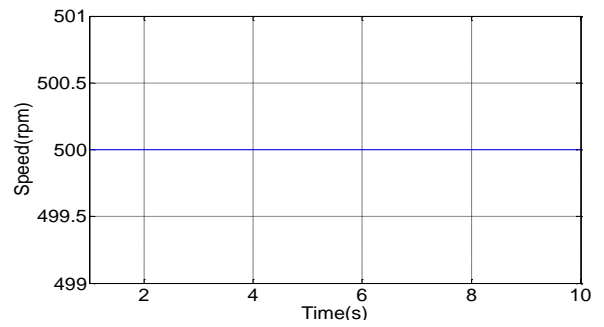


(d) Stator current

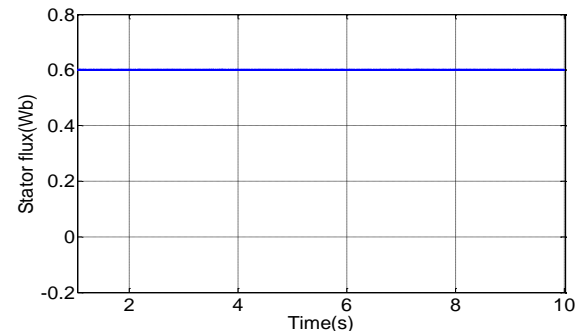
Fig.10. Simulated waveforms using the proposed scheme in a B4 inverter fed behaviors during a speed-reversal maneuver at 50% rated load torque ($\lambda_0 = 1$). IM .



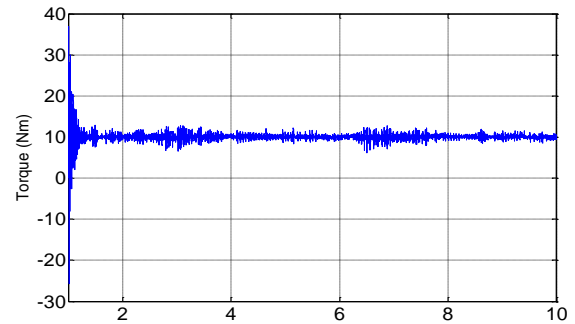
(a) Capacitor voltages



(b) Speed

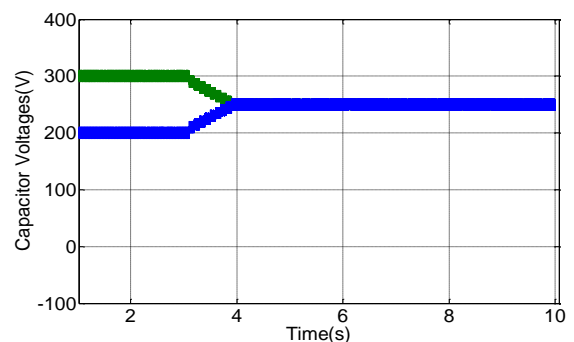


(a) Stator flux

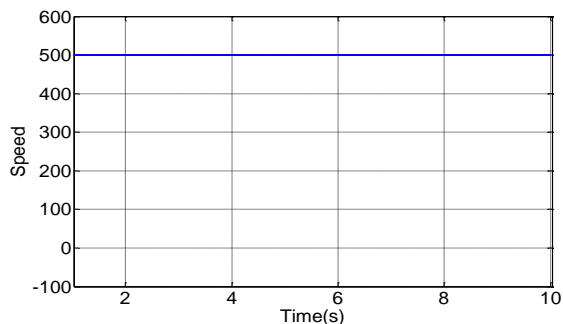


(a) Developed torque

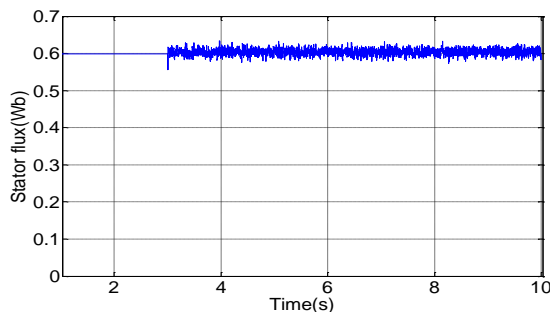
Fig.11. simulated two capacitor voltages with offset suppression method. Behaviors during a voltage suppression method applied ($\lambda_{dc} = 1000$).



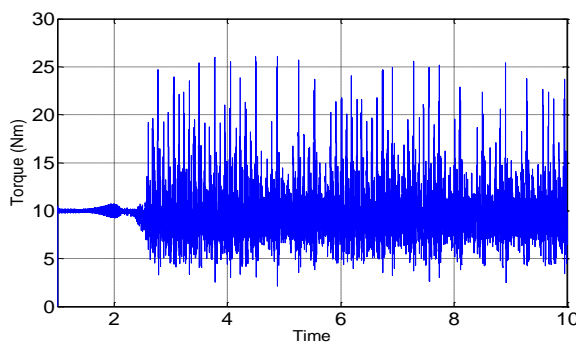
(a) Capacitor voltages



(a) speed



(c) Stator flux



(d) Developed torque

Fig.12. Simulated two capacitor voltages with offset suppression method behaviors during a voltage suppression method applied ($\lambda_{dc} = 2000$).

Results comparison table:

S.No.	Variable Name	Proposed THD (%)	Extension THD (%)
1.	Stator current when $\lambda_0 = 3$	0.83	0.19
2.	Stator current when $\lambda_0 = 1$	7.19%	4.68%
3.	Stator current	0.65	0.06

	when =		
4.	Stator current when = 2000	0.66	0.08

VII. CONCLUSION

In this paper, the uncommon issues on utilizing the acclaimed PTC control conspire for a B4 inverter-fed IM drives are investigated also, talked about. The voltage vectors of the B4 inverter under the conversion of the two dc-link capacitor voltages are inferred for exact expectation and control of the torque and stator flux. The proposed B4 inverter-fed IM drive has been discovered satisfactory for best modern variable-speed-drive applications considering its cost diminishment and other characteristic profitable components. Unquestionably, the extra work is still stayed to grow more productive PTC plan and answers the rest of the inquiries: the power toward parameter deviation, parameter affectability of this plan, for the parameter qualities may differ in the motor drives, while in different cases it is hard to get an exact value of the parameters.

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